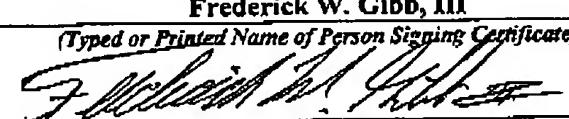


CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)				Docket No. BUR920030092US1
Applicant(s): Allen et al.				
Application No. 10/604,987	Filing Date 08/29/03	Examiner Siek, Vuthe	Group Art Unit 2822	
Invention: THE USE OF A LAYOUT-OPTIMIZATION TOOL TO INCREASE THE YIELD AND RELIABILITY OF VLSI DESIGNS				
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Application Number	10/604,987
Filing Date	08/29/03
First Named Inventor	Allen et al.
Art Unit	2822
Examiner Name	Sick, Vuthe
Attorney Docket Number	BUR920030092US1

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